IN THE CLAIMS

Please cancel Claims 1 and 7 without prejudice or disclaimer.

Claim 1 (cancelled).

Claim 2 (currently amended): Oscillator circuit comprising:

an LC resonant circuit (1),

an activating component (2) connected to the LC resonant circuit (1), which serves to compensate for the losses occurring in the LC resonant circuit (1), whereby the series-configuration of both the LC resonant circuit (1) and the activating component (2),

a current-defining element which connects the LC resonant circuit and the activating component, which sets a current flowing through the activating component (2), between a first voltage (VDD) and a second voltage (VSS), which is different from the first voltage (VDD), and

wherein the current-defining element includes an ohmic resistance (R1) and wherein the oscillator circuit furthermore comprises a control loop circuit, which serves to set the current flowing through the activating component (2) to a pre-determined constant value,

~~Oscillator circuit according to Claim 1,~~ wherein the control loop circuit comprises a copy (3) of the activating component.

Claim 3 (currently amended): Oscillator circuit according to Claim ~~1~~ 2, wherein the control loop circuit furthermore comprises a reference current source (Iref), which is connected to the copy (3) of the activating component.

Claim 4 (currently amended): Oscillator circuit comprising:

an LC resonant circuit (1),

an activating component (2) connected to the LC resonant circuit (1), which serves to compensate for the losses occurring in the LC resonant circuit (1), whereby the series-configuration of both the LC resonant circuit (1) and the activating component (2),

a current-defining element which connects the LC resonant circuit and the activating component, which sets a current flowing through the activating component (2), between a first voltage (VDD) and a second voltage (VSS), which is different from the first voltage (VDD), and

wherein the current-defining element includes an ohmic resistance (R1) and wherein the oscillator circuit furthermore comprises a control loop circuit, which serves to set the current flowing through the activating component (2) to a pre-determined constant value,

~~Oscillator circuit according to Claim 1,~~ wherein said oscillator circuit furthermore comprises an operational amplifier (4), to one of whose inputs is applied the voltage present at the activating component (2), and to whose other input is applied the voltage present at ~~the~~ a copy of the activating component (3), and whose output signal controls a transistor (5) that is connected in parallel to the series-connected control loop circuit, consisting of the LC resonant circuit (1) and the activating component (2).

Claim 5 (currently amended): Oscillator circuit according to Claim ~~1~~ 2 wherein said oscillator circuit furthermore comprises a low-pass filter (C2, R2).

Claim 6 (currently amended): Oscillator circuit as in Claim ~~1~~ 2, wherein the ohmic resistor (R1) is integrated.

Claim 7 (cancelled).

Claim 8 (currently amended): Oscillator circuit comprising:

an LC resonant circuit (1),

an activating component (2) connected to the LC resonant circuit (1), which serves to compensate for the losses occurring in the LC resonant circuit (1), whereby the series-configuration of both the LC resonant circuit (1) and the activating component (2),

a current-defining element which connects the LC resonant circuit and the activating component, which sets a current flowing through the activating component (2), between a first voltage (VDD) and a second voltage (VSS), which is different from the first voltage (VDD), and

wherein the current-defining element includes an ohmic resistance (R1) and wherein the oscillator circuit furthermore comprises a control loop circuit, which serves to set the current flowing through the activating component (2) to a pre-determined constant value,

wherein the activating component comprises the MOS FETs (6, 7), and

~~Oscillator circuit according to claim 7,~~ where the length of the gates of the MOS FETs (8, 9) of ~~the~~ a copy (3) of the activating component is greater by a specific factor than the length of the gates of the MOS FETs (6, 7) of the activating component (2).

Claim 9 (currently amended): Oscillator circuit as in Claim ~~1~~ 2, wherein the reference current (Iref) is lower by a specific factor than the current flowing through the resistor (R1).

Claim 10 (currently amended): Oscillator circuit as in Claim ~~1~~ 2, wherein all circuit elements are in integrated form.

Claim 11 (currently amended): Oscillator circuit as in Claim ~~1~~ 2, wherein the activating component (2) comprises a first MOS FET (6) and a second MOS FET (7), whose source terminals are each connected to the resistor, and whose drain terminals are each connected to the LC resonant circuit (1), whereby the gate terminal is in each case connected to the drain terminal of the corresponding other MOS FET.

Claim 12 (currently amended): Oscillator circuit as in Claim ~~1~~ 2, wherein the first voltage is a supply voltage (VDD) and the second voltage is ground potential (VSS).

Claim 13 (original): Oscillator circuit according to Claim 12, wherein the LC resonant circuit (1) is connected to the supply voltage (VDD), and the resistor (R1) is connected between the activating circuit and ground potential (VSS).